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;; FILENAME: LPF2\_2.inc

;; Version: 2.5, Updated on 2010/9/19 at 23:40:48

;; Generated by PSoC Designer 5.4.3191

;;

;; DESCRIPTION: Assembler declarations for the LPF2 user module interface

;;-----------------------------------------------------------------------------

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;;\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

include "m8c.inc"

;--------------------------------------------------

; Register constants and masks

;--------------------------------------------------

LPF2\_2\_OFF: equ 0

LPF2\_2\_LOWPOWER: equ 1

LPF2\_2\_MEDPOWER: equ 2

LPF2\_2\_HIGHPOWER: equ 3

LPF2\_2\_FEEDBACK\_16: equ 0

LPF2\_2\_FEEDBACK\_32: equ 1

LPF2\_2\_POLARITY\_INVERTING: equ 0

LPF2\_2\_POLARITY\_NON\_INVERTING: equ 1

LPF2\_2\_AMD\_MSK: equ 0x70

LPF2\_2\_AMD\_VAL: equ 0x0

;--------------------------------------------------

; Registers used by LPF2\_2

;--------------------------------------------------

LPF2\_2\_FLIN\_CR0: equ 88h ; SCA Block Control Register 0

LPF2\_2\_FLIN\_CR1: equ 89h ; SCA Block Control Register 1

LPF2\_2\_FLIN\_CR2: equ 8ah ; SCA Block Control Register 2

LPF2\_2\_FLIN\_CR3: equ 8bh ; SCA Block Control Register 3

LPF2\_2\_FLOUT\_CR0: equ 8ch ; SCA Block Control Register 0

LPF2\_2\_FLOUT\_CR1: equ 8dh ; SCA Block Control Register 1

LPF2\_2\_FLOUT\_CR2: equ 8eh ; SCA Block Control Register 2

LPF2\_2\_FLOUT\_CR3: equ 8fh ; SCA Block Control Register 3

; UM Cap naming convention per Gregorian and Temes, p293

LPF2\_2\_FILT\_C1\_REG: equ 88h

LPF2\_2\_FILT\_C2\_REG: equ 89h

LPF2\_2\_FILT\_C3\_REG: equ 8ch

LPF2\_2\_FILT\_C4\_REG: equ 8eh

LPF2\_2\_FILT\_CA\_REG: equ 88h

LPF2\_2\_FILT\_CB\_REG: equ 8ch

LPF2\_2\_AMD\_REG: equ 63h

; end of file LPF2\_2.inc